

IN THE CLAIMS:

Please amend the claims as follows:

1. (Previously Presented): A method of driving a liquid crystal display device during one display frame, comprising the steps of:

applying one of a high-level common voltage and a low-level common voltage to a plurality of liquid crystal cells of the liquid crystal display device to write data into the liquid crystal cells within a time interval shorter than one display frame interval;

applying a reference common voltage to the plurality of liquid crystal cells after applying the one of the high-level common voltage and the low-level common voltage; and

turning on a backlight after said data writing to display an image.

2. (Previously Presented): The method according to Claim 1, wherein after applying one of the high-level common voltage and the low-level common voltage, the liquid crystal cells respond according to the data written between the time when the data is written and when the backlight is turned on.

3. (Previously Presented): The method according to Claim 1, wherein the reference common voltage is lower than the high-level common voltage and greater than the low-level common voltage.

4. (Currently Amended): The method according to Claim 1, further comprising the step of re-aligning the liquid crystal cells after the step of turning on the backlight.

5. (Currently Amended): The method according to Claim 4, wherein at the step of re-aligning, one of the high-level common voltage ~~or~~ and the low-level common voltage is applied.

6. (Original): The method according to Claim 4, wherein at the step of re-aligning, a common voltage having a polarity opposite to the common voltage applied when the data is written is applied.

7. (Original): The method according to Claim 1, wherein when data is being written, an effective voltage remaining in the liquid crystal cell is larger than a data voltage applied to the liquid crystal cell.

8. (Original): The method according to Claim 1, wherein the high-level common voltage is equal to or more than +15V.

9. (Original): The method according to Claim 8, wherein the high-level common voltage is equal to a gate high voltage applied to a gate electrode of a thin film transistor of the liquid crystal cell.

10. (Original): The method according to Claim 1, wherein the low-level common voltage is equal to or less than -5V.

11. (Original): The method according to Claim 10, wherein the low-level common voltage is equal to a gate low voltage applied to a gate electrode of a thin film transistor in the liquid crystal cell.

12. (Currently Amended): The method according to Claim 1, wherein the driving method is applied to one of an optically compensated bend mode, a ferroelectric liquid crystal mode, and a twisted nematic mode liquid crystal display device.

13. (Currently Amended): A method of driving a liquid crystal display device during one display frame, the method comprising the steps of:

inputting data signals to a plurality of liquid crystal cells;

allowing the liquid crystal cells to respond to the applied data signals; and

applying a reference common voltage to the plurality of the liquid crystal cells after the allowing of the liquid crystal cells to respond,

wherein one of a high-level common voltage and a low-level common voltage is applied to the plurality of liquid crystal cells during the inputting step.

14. (Previously Presented): The method according to claim 13, wherein the reference common voltage is lower than the high-level common voltage and greater than the low-level common voltage.

15. (Currently Amended): The method according to claim 13, further comprising the step of [[:]] turning on a backlight after the step of applying the reference common voltage.

16. (Previously Presented): The method according to claim 15, wherein one of the high-level and low-level common voltages is applied to the liquid crystal cells after the step of turning on.

17. (Currently Amended): The method according to claim 15, further comprising the step of [[:]] re-aligning the liquid crystal cells after the step of turning on.

18. (Previously Presented): The method according to claim 17, wherein one of the high-level and low-level common voltages is applied to the liquid crystal cells during the step of re-aligning.

19. (Previously Presented): The method according to claim 17, wherein during the step of re-aligning, a common voltage applied to the liquid crystal cells has a polarity opposite to the common voltage during the step of inputting.

20. (Previously Presented): The method according to claim 13, wherein the high-level common voltage is equal to or more than +15V.

21. (Original): The method according to claim 13, wherein the high-level common voltage is equal to a gate high voltage applied to a gate electrode of a thin film transistor of the liquid crystal cell.

22. (Original): The method according to claim 13, wherein the low-level common voltage is equal to or less than -5V.

23. (Original): The method according to claim 13, wherein the low-level common voltage is equal to a gate low voltage applied to a gate electrode of a thin film transistor in the liquid crystal cell.

24. (Currently Amended): The method according to claim 13, wherein the driving method is applied to one of an optically compensated bend mode, a ferroelectric liquid crystal mode, and a twisted nematic mode liquid crystal display device.